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Patent, Trademark and Copyright Causes,
 Unfair Competition, Trade Secrets,
 Licensing and Litigation

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TO: United States Patent and Trademark Office
 PATENT PROSECUTION HIGHWAY PILOT PROGRAM
 Attn: Magdalen Greenlief
 Facsimile: 571-273-0125

Date: September 27, 2007

From: THE NATH LAW GROUP, U.S. Filing Dept.
 Customer no. 20529

RE: U.S. Serial Number 10/527,820
 Title: ACCESS METHOD
 Inventor: TAKAGI
 Our reference Number: 26642U

NO. OF PAGES (including this page): 30

COMMENTS AND/OR SPECIAL INSTRUCTIONS:

Please direct any questions to Jerry Meyer (telephone: 703-548-6284).

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REQUEST FOR PARTICIPATION IN THE PATENT PROSECUTION HIGHWAY (PPH) PILOT PROGRAM BETWEEN THE JPO AND THE USPTO

Application No.:	10/527,820	First Named Inventor:	Yoshihiko TAKAGI
Filing Date:	July 15, 2004	Attorney Docket No.:	26642U
Title of the Invention:	ACCESS METHOD		

THIS REQUEST FOR PARTICIPATION IN THE PPH PILOT PROGRAM MUST BE FAXED TO:
 THE OFFICE OF THE COMMISSIONER FOR PATENTS AT 571-273-0125 DIRECTED TO THE ATTENTION OF MAGDALEN GREENLIEF

APPLICANT HEREBY REQUESTS PARTICIPATION IN THE PATENT PROSECUTION HIGHWAY (PPH) PILOT PROGRAM AND PETITIONS TO MAKE THE ABOVE-IDENTIFIED APPLICATION SPECIAL UNDER THE PPH PILOT PROGRAM.

The above-identified application validly claims priority under 35 U.S.C. 119(a) and 37 CFR 1.55 to one or more corresponding JPO application(s).

The JPO application number(s) is/are: 2003-275672, 2004-197453 and 2006-341650

The filing date of the JPO application(s) is/are: July 16, 2003, July 2, 2004 and December 19, 2006

I. List of Required Documents:

- A copy of all JPO office actions excluding "Decision to Grant a Patent" in the above-identified JPO application(s).
 - ☒ Is attached.
 - ☐ Is available via Dossier Access System. Applicant hereby requests that the USPTO obtain these documents via the Dossier Access System.

*It is not necessary to submit a copy of the "Decision to Grant a Patent" and an English translation thereof.
- A copy of all claims which were determined to be patentable by the JPO in the above-identified JPO application(s).
 - ☒ Is attached.
 - ☐ Is available via Dossier Access System. Applicant hereby requests that the USPTO obtain these documents via the Dossier Access System.
- English translations of the documents in a. and b. above along with a statement that the English translations are accurate are attached.

Information disclosure statement listing the documents cited in the JPO office actions is attached.

Copies of all documents are attached except for U.S. patents or U.S. patent application publications.

[Page 1 of 2]

This collection of information is required by 35 U.S.C. 119, 37 CFR 1.55, and 37 CFR 1.102(d). The information is required to obtain or retain a benefit by the public, which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37CFR 1.11 and 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. FAX COMPLETED FORMS 70: Office of the Commissioner for Patents at 571-273-0125, Attention: Magdalen Greenlief.

Name (Print/Typed) <i>Serald Meyer</i>	Registration Number <i>41,194</i>
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WARNING:

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Petitioner/applicant is advised that the record of a patent application is submitting them to the USPTO.

Petitioner/applicant is advised that the record of a patent application is available to the public after publication of the application (unless a non-publication request in compliance with 37 CFR 1.213(a) is made in the application) or issuance of a patent. Furthermore, the record from an abandoned application may also be available to the public if the application is authorization forms PTO-2038 submitted for payment purposes are not retained in the application

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5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.
6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).
7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.
8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspection or an issued patent.
9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

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VERIFICATION OF A TRANSLATION

I, Takashi KISO
of 5th Floor, Shintoshicenter Bldg, 24-1, Tsurumaki 1-chome,
Tama-shi, Tokyo 206-0034 Japan

declare that I am well acquainted with both the Japanese and English
languages, and that the attached is an accurate translation, to the best of
my knowledge and ability, of the claim portion of Japanese Patent
Application No. 2006-341650 which is a Divisional Application of No.
2004-197453.

Signature:

Takashi Kiso

Takashi KISO

Date: August 30, 2007

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[SCOPE OF CLAIMS FOR PATENT]

[Claim 1]

An access method for an apparatus to gain access to a specific area assigned to an area number of a memory device, the method comprising the steps of:

the apparatus transmitting designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an access area of the memory device, to the memory device;

the apparatus transmitting verification information generated based on the designation information, together with a processing command for the access area;

the memory device verifying the verification information using the designation information; and

the memory device executing the processing command when the verification succeeds.

20 [Claim 2]

An access method for an apparatus to gain access to a specific area assigned to an area number of a memory device, the method comprising the steps of:

the apparatus sharing a verification key with the memory device;

the apparatus transmitting designation information including the area number, an access start

offset in the specific area associated with the area number
and a size of data to access and designating an access
area of the memory device, to the memory device;

the apparatus transmitting verification data
5 obtained by encrypting the designation information using
the verification key, together with a processing command
for the access area;

the memory device verifying the verification data
using the designation information and the verification
10 key; and

the memory device executing the processing command
when the verification succeeds.

[Claim 3]

15 An access method for an apparatus to gain access
to a specific area assigned to an area number of a memory
device, the method comprising the steps of:

the apparatus sharing a verification key with the
memory device;

20 the apparatus requesting transmission of random
numbers to the memory device;

the memory device transmitting random numbers to
the apparatus;

the apparatus transmitting designation
25 information including the area number, an access start
offset in the specific area associated with the area number
and a size of data to access and designating an access

area of the memory device, to the memory device;

the apparatus transmitting verification data obtained by encrypting verification information including the random numbers and the designation information, together with a processing command for the access area;

the memory device verifying the verification data using the random numbers, the designation information and the verification key; and

the memory device executing the processing command when the verification succeeds.

[Claim 4]

A memory device whose data of a specific area assigned to an area number is read and written by an apparatus, the memory device comprising:

a processing command receiver that receives designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an area to access and receives verification information generated based on the designation information and a processing command including a command for read or write;

a designation information verifier that performs verification processing on the verification information using the designation information;

a storage area that stores data;

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a storage area access section that performs read or write from/in a designated area of the storage area according to the command for processing, when the verification processing succeeds;

5 a data transmitter that transmits data read by the storage area access section to the apparatus; and

a data receiver that receives data to write from the apparatus.

10 [Claim 5]

The memory device according to claim 4, wherein the designation information verifier generates random numbers in response to a request by the apparatus, holds the random numbers and transmits the random numbers to
15 the apparatus.

[Claim 6]

The memory device according to claim 5, wherein the designation information verifier performs the
20 verification processing using the verification information and a verification key.

[Claim 7]

The memory device according to claim 6, further
25 comprising:

a verification key sharing section that shares the verification key with the apparatus.

[Claim 8]

An information apparatus that reads and writes data of a specific area assigned to an area number from/in a memory device, the information apparatus comprising:

5 a designation information determiner that determines designation information including an access start offset for reading and writing data from/in the specific area, a size of data for performing read and write and the area number and designating the access area;

a verification information generator that performs processing for generating verification information based on the designation information;

15 a processing command transmitter that transmits the designation information to the memory device and separately transmits the verification information and a processing command for reading or writing data;

a data transmitter that transmits data to the memory device when the processing command is write; and

20 a data receiver that receives data from the memory device when the processing command is read.

[Claim 9]

The information apparatus according to claim 8, wherein the detection information verifier requests transmission of random numbers to the memory device and receives the random numbers from the memory device.

[Claim 10]

The information apparatus according to claim 9,
wherein the verification information generator performs
5 the processing for generating the verification
information using the designation information and a
verification key.

[Claim 11]

10 The information apparatus according to claim 10,
further comprising:

a verification key sharing section that shares the
verification key with the memory device.

15 [Claim 12]

An access method for an apparatus to gain access
to a specific area assigned to an area number of a memory
device, the method comprising:

the apparatus transmitting designation
20 information including the area number, an access start
offset in the specific area associated with the area number
and a size of data to access and designating an access
area of the memory device, to the memory device;

the apparatus transmitting verification data
25 obtained by encrypting the designation information using
the verification key, together with a processing command
for the access area to the memory device;

the memory device verifying the verification data using the designation information and the verification key; and

the memory device executing the processing command
5 when the verification succeeds.

[Claim 13]

An access method for an apparatus to gain access to a specific area assigned to an area number of a memory
10 device, the method comprising the steps of:

the apparatus requesting transmission of random numbers to the memory device;

the memory device transmitting random numbers to the apparatus;

15 the apparatus transmitting designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an access area of the memory device, to the memory device;

20 the apparatus transmitting verification data obtained by encrypting the designation information using a verification key, together with a processing command for the access area to the memory device;

the memory device verifying the verification data
25 using the random numbers, the designation information and the verification key; and

the memory device executing the processing command

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when the verification succeeds.

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最終頁に続く

(54) 【発明の名称】 アクセス方法、メモリデバイス、および情報機器

(57) 【特許請求の範囲】

【請求項1】

機器からメモリデバイスの領域番号に割り当てられた特定の領域に対するアクセス方法であって、

前記機器が、前記領域番号と、前記領域番号に関連づけられた前記特定の領域内におけるアクセス開始オフセットと、アクセスするデータのサイズと、を含む、前記メモリデバイスへのアクセス先を指定する指定情報を前記メモリデバイスへ送信するステップと、

前記機器が、前記指定情報に基づいて生成した検証情報を、前記アクセス先への処理命令とともに送信するステップと、

前記メモリデバイスが、前記指定情報を用いて前記検証情報を検証するステップと、

前記検証にて成功した場合、前記メモリデバイスが、前記処理命令を実行するステップと、を有するアクセス方法。

【請求項2】

機器からメモリデバイスの領域番号に割り当てられた特定の領域に対するアクセス方法であって、

前記機器が、前記メモリデバイスとの間で、検証用鍵を共有化するステップと、

前記機器が、前記領域番号と、前記領域番号に関連付けられた前記特定の領域内におけるアクセス開始オフセットと、アクセスするデータのサイズと、を含む、前記メモリデバイスへのアクセス先を指定する指定情報を前記メモリデバイスへ送信するステップと、

前記機器が、前記指定情報を前記検証用鍵で暗号化した検証データを、前記アクセス領

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域への処理命令とともに送信するステップと、

前記メモリデバイスが、前記指定情報と前記検証用鍵とを用いて、前記検証データを検証するステップと、

前記検証にて成功した場合、前記メモリデバイスが、前記処理命令を実行するステップと、

を有するアクセス方法。

【請求項 3】

機器からメモリデバイスの領域番号に割り当てられた特定の領域に対するアクセス方法であって、

前記機器が、前記メモリデバイスとの間で、検証用鍵を共有化するステップと、

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前記機器が、前記メモリデバイスに乱数の送信を要求するステップと、

前記メモリデバイスが、前記機器に対して乱数を送信するステップと、

前記機器が、前記領域番号と、前記領域番号と関連付けられた前記特定の領域内におけるアクセス開始オフセットと、アクセスするデータのサイズと、を含む、前記メモリデバイスへのアクセス先を指定する指定情報を前記メモリデバイスへ送信するステップと、

前記機器が、前記乱数と前記指定情報とを含む検証情報を前記検証用鍵で暗号化した検証データを前記アクセス先への処理命令とともに送信するステップと、

前記メモリデバイスが、前記乱数と前記指定情報と前記検証用鍵とを用いて、前記検証データを検証するステップと、

前記検証にて成功した場合、前記メモリデバイスが、前記処理命令を実行するステップと、

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を有するアクセス方法。

【請求項 4】

機器から、領域番号に割り当てられた特定の領域のデータが読み書きされるメモリデバイスであって、

前記領域番号と、前記領域番号と関連付けられた前記特定の領域内におけるアクセス開始オフセットと、アクセスするデータのサイズと、を含む、アクセス先を指定する指定情報を受信するとともに、前記指定情報に基づいて生成された検証情報と読み出し又は書き込み命令を含む処理命令とを受信する処理命令受信手段と、

前記指定情報を用いて前記検証情報の検証処理を行う指定情報検証手段と、

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データを格納する記憶領域と、

前記検証処理が成功した場合に、前記処理命令に応じて、前記記憶領域の前記指定領域に対する読み出し又は書き込みを行う記憶領域アクセス手段と、

前記記憶領域アクセス手段が読み出したデータを前記機器に送信するデータ送信手段と、

前記機器から書き込みデータを受信するデータ受信手段と、

を備えるメモリデバイス。

【請求項 5】

前記指定情報検証手段は、前記機器の要求により、乱数を生成し、前記乱数を保持し、前記乱数を前記機器に送信する、

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請求項 4 記載のメモリデバイス。

【請求項 6】

前記指定情報検証手段は、前記検証処理を、前記検証情報と検証用鍵を用いて行う、

請求項 5 記載のメモリデバイス。

【請求項 7】

前記機器との間で前記検証用鍵を共有する検証用鍵共有手段をさらに備える、請求項 6 記載のメモリデバイス。

【請求項 8】

メモリデバイスに対して領域番号に割り当てられた特定の領域のデータの読み書きする情報機器であって、

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前記特定の領域における、データの読み出し又は書き込みするアクセス開始オフセットと、読み出し又は書き込みするデータのサイズと、前記領域番号と、を含む、アクセス先を指定する指定情報を決定する指定情報決定手段と、

前記指定情報に基づいて前記検証情報の生成処理を行う検証情報生成手段と、

前記指定情報を前記メモリデバイスに送信するとともに、別途、前記検証情報と、データの読み出し又は書き込みの処理命令と、を送信する処理命令送信手段と、

前記処理命令が書き込みの場合は、前記メモリデバイスにデータを送信するデータ送信手段と、

前記処理命令が読み出しの場合は、前記メモリデバイスからデータを受信するデータ受信手段と、

を備える情報機器。

【請求項 9】

前記検証情報生成手段は、前記メモリデバイスに対し乱数の送信を要求し、前記メモリデバイスから前記乱数を受信する、

請求項 8 記載の情報機器。

【請求項 10】

前記検証情報生成手段は、前記検証情報の生成処理を、前記指定情報と検証用鍵とを用いて行う、

請求項 9 記載の情報機器。

【請求項 11】

前記メモリデバイスとの間で前記検証用鍵を共有する検証用鍵共有手段を備える、

請求項 10 記載の情報機器。

【請求項 12】

機器からメモリデバイスの領域番号に割り当てられた特定の領域に対するアクセス方法であって、

前記機器が、前記領域番号と、前記領域番号と関連付けられた前記特定の領域内におけるアクセス開始オフセットと、アクセスするデータのサイズと、を含む、前記メモリデバイスへのアクセス先を指定する指定情報を前記メモリデバイスへ送信するステップと、

前記機器が、前記指定情報を検証用鍵で暗号化した検証データを前記アクセス先への処理命令とともに前記メモリデバイスへ送信するステップと、

前記メモリデバイスが、前記検証データを前記指定情報と検証用鍵とを用いて検証するステップと、

前記検証に成功した場合、前記メモリデバイスが、前記処理命令を実行するステップと

を有するアクセス方法。

【請求項 13】

機器からメモリデバイスの領域番号に割り当てられた特定の領域に対するアクセス方法であって、

前記機器が、前記メモリデバイスに乱数の送信を要求するステップと、

前記メモリデバイスが、前記機器に対して乱数を送信するステップと、

前記機器が、前記領域番号と、前記領域番号と関連付けられた前記特定の領域内におけるアクセス開始オフセットと、アクセスするデータのサイズと、を含む、前記メモリデバイスへのアクセス先を指定する指定情報を前記メモリデバイスへ送信するステップと、

前記機器が、前記指定情報を検証用鍵で暗号化した検証データを前記アクセス先への処理命令と併せて前記メモリデバイスへ送信するステップと、

前記メモリデバイスが、前記乱数と前記指定情報と前記検証用鍵を用いて前記検証データを検証するステップと、

前記検証に成功した場合、前記メモリデバイスが、前記処理命令を実行するステップと

を有するアクセス方法。

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FAX: 042-338-4605

P. 007

esp@cenet document view

1 ページ

No title available**Publication number:** JP2007133892**Publication date:** 2007-05-31**Inventor:****Applicant:****Classification:****- International:** G06F12/14; G06F21/22; G06F21/24; G06K17/00; G06K19/073;
G06F12/14; G06F21/00; G06K17/00; G06K19/073;**- European:****Application number:** JP20080341650 20061219**Priority number(s):** JP20030275672 20030716; JP20080341650 20061219**Report a data error here**

Abstract not available for JP2007133892

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2007/08/30/木 18:32

FAX#042-338-4605

P.038

esp@cenet Family list view

1 ページ

Family list

8 family members for: JP2007133892

Derived from 8 applications

- 1 Access method
Inventor: TAKAFUMI TAKAGI YOSHIHIKO KIKU (JP) Applicant: MATSUSHITA ELECTRIC IND CO LTD (JP).
EC: IPC: G06F12/14; G06F12/14; (IPC1-7): G06F12/14
Publication info: CN1701310 A - 2005-11-23
- 2 ACCESS METHOD
Inventor: TAKAGI YOSHIHIKO (JP); KIKUCHI TAKAFUMI (JP) Applicant: MATSUSHITA ELECTRIC IND CO LTD (JP)
EC: G06F21/00N1D; G06F21/00N9A; (+3) IPC: G06F12/14; G06F21/00; G06F21/06 (+16)
Publication info: EP1560120 A1 - 2005-08-03
- 3 ACCESS METHOD
Inventor: TAKAGI YOSHIHIKO; KIKUCHI TAKAFUMI Applicant: MATSUSHITA ELECTRIC IND CO LTD
EC: G06F21/00N1D; G06F21/00N9A; (+3) IPC: G06F12/14; G06F21/00; G06F21/06 (+18)
Publication info: JP2005050320 A - 2005-02-24
- 4 No title available
Inventor: Applicant:
EC: IPC: G06F12/14; G06F21/22; G06F21/24 (+6)
Publication info: JP2007133892 A - 2007-05-31
- 5 ACCESS METHOD
Inventor: TAKAGI YOSHIHIKO (JP); KIKUCHI TAKAFUMI (JP) Applicant: MATSUSHITA ELECTRIC IND CO LTD (JP)
EC: G06F21/00N1D; G06F21/00N9A; (+3) IPC: G06F12/14; G06F21/00; G06F21/06 (+14)
Publication info: KR20060024317 A - 2006-03-16
- 6 ACCESS METHOD
Inventor: TAKAGI YOSHIHIKO (JP); KIKUCHI TAKAFUMI (JP) Applicant: MATSUSHITA ELECTRIC IND CO LTD (JP)
EC: G06F21/00N1D; G06F21/00N9A; (+3) IPC: G06F12/14; G06F12/00; G06F21/00 (+16)
Publication info: KR20070009740 A - 2007-01-18
- 7 Access method
Inventor: TAKAGI YOSHIHIKO (JP); KIKUCHI TAKAFUMI (JP) Applicant:
EC: G06F21/00N1D; G06F21/00N9A; (+3) IPC: G06F12/14; G06F21/00; G06F21/06 (+15)
Publication info: US2005246546 A1 - 2005-11-03
- 8 ACCESS METHOD
Inventor: TAKAGI YOSHIHIKO; KIKUCHI TAKAFUMI Applicant: MATSUSHITA ELECTRIC IND CO LTD (JP);
TAKAGI YOSHIHIKO; (+1)
EC: G06F21/00N1D; G06F21/00N9A; (+3) IPC: G06F12/14; G06F21/00; G06F21/06 (+16)
Publication info: WO2005008502 A1 - 2005-01-27

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(English Translation)

**NOTICE OF REASON FOR REJECTION**

Dispatch Date	February 6, 2007
Japanese Patent Application Number	2006-341650
Drafting Date	January 30, 2007
Examiner of Patent Office	Shigeyuki SAKURAI 2945 5M00
Attorney	Kimihito WASHIDA
Applied Provision	Section 29(2)

This application should be refused for the reason mentioned below. If the applicant has any argument against the reason, such argument should be submitted within 60 days from the date on which this notification was dispatched.

REASON

The inventions in the claims listed below of the subject application should not be granted a patent under the provision of Patent Law Section 29 (2) since they could have easily been made by persons who have common knowledge in the technical field to which the inventions pertain, on the basis of inventions described in the publications listed below which were distributed, or inventions made accessible to public through electric telecommunication line, in Japan or foreign countries prior to the filing of the subject application.

NOTE (see the list of cited references)

Claims: 1-13

Cited references: 1-3

Remarks:

As described in FIG. 1 and FIGs. 3-5 of the cited reference 1 and also in the descriptions thereof, it is a well known technique that a processing command of an access region and an authentication information regarding this processing command are transmitted together from an apparatus to a memory device, and after carrying out the authentication processing in the memory device the processing to the access region is carried out.

Moreover, as described in FIGs. 3-4, and FIG. 12 (b) of the cited reference 2 and also in the descriptions thereof, transmitting an access area designation information including the area number, access start offset, data size from an apparatus to a memory device is also usually carried out, and that in the above-described cited reference 1 the access area designation information described in the cited reference 2 is transmitted to designate the access destination can be easily done by those skilled in the art.

Moreover, a configuration of providing a command for reading the area number from the apparatus to the memory device and of transmitting the area number from the memory device to the apparatus is also commonly employed.

In addition, in the communications for carrying out data verification, the use of a session key (a temporarily shared key, and corresponding to a verification key which the apparatus and memory device of the subject application share) which the apparatus and the memory device share is also a well known technique as described in FIGs. 1 and 2 of the cited reference 3 and also in the descriptions thereof. The use of a random number in data verification is also a well known technique.

List of Cited Reference(s)

1. Japanese Patent Application Laid-Open No. HEI11-306088
2. Japanese Patent Application Laid-Open No. HEI6-302180
3. Japanese Patent Application Laid-Open No. 2001-118034

If any new reason for refusal is found in future, a further notice will be issued.

Disclaimer:

This English translation is produced by machine translation and may contain errors. The JPO, the INPIT, and those who drafted this document in the original language are not responsible for the result of the translation.

Notes:

1. Untranslatable words are replaced with asterisks (****).
2. Texts in the figures are not translated and shown as it is.

Translated: 20:36:22 JST 10/01/2007

Dictionary: Last updated 09/07/2007 / Priority:

Decision to Grant a Patent

Application number: Application for patent 2006-341650

Date of Drafting: Heisei 19(2007) May 10

Patent examiner: SAKURAI, Shigeyuki 2945 5M00

Title of invention: The access method, a memory device, and information machines and equipment

The number of claims: 13

Applicant: MATSUSHITA ELECTRIC INDUSTRIAL CO. LTD.

Representative: WASHIDA, Kimihito

This application is to be granted a patent as there is no reason for refusal.

Director General(p.p.) Director(p.p.) Examiner Assistant examiner Manager for Determination
of Classification TAKASE, Tsutomu SAKURAI, Shigeyuki HAMAGISHI, Hiroaki 9069 2945
9845

1. Distinction of Patent: Division

2. Reference documents: **

3. Application of Patent Law, Section 30: Nothing

4. Change of Title of Invention: Nothing

5. International Patent Classification (IPC)

G06F 12/14 510D, G06K 17/00 E, G06F 12/14 520A, G06K 19/00 P, G06F 9/06 660G

6. Deposition of Microorganism

7. Display of Purport that Retroactivity of Filing Date is not Accepted

Decision to Grant a Patent(Memorandum)

Application number: Application for patent 2006-341650

1. Technical Fields to Be Searched (IPC, DB Name)

G06F 12/14 G06K 17/00 G06K 19/073 G06F 21/22 G06F 21/24

2. Reference patent documents

JP,11-306088,A (JP, A) JP,06-302180,A (JP, A) JP,2001-118034,A (JP, A) JP,2003-091704,A (JP, A) JP,01-147686,A (JP, A) JP,07-078126,A (JP, A)

3. Reference books and magazines

[Translation done.]

1

US Patent Application No.10/527,820

Client Ref. P35520-02

Your Ref. P26642U

Our Ref. 2F04119-US

5

COMMENTS

We would like to file the following preliminary amendment for the present application prior to the examination.

10

In addition, we would like to request participation in the Patent Prosecution Highway (PPH) Pilot Program between the JPO and the USPTO. Enclosed please find a request for participation in the Patent Prosecution Highway (PPH) Pilot Program between the JPO and the USPTO, and scope of claims for patent of patentable claims in JP application of which JPO application number is 2006-341650. This application was issued on June 1, 2007 (P3963938) and it is a divisional application of a parent application 2004-197453. The application 2004-197453 is the national priority application on the basis of 2003-275672. Please note that claims of US application amended this time correspond to the similarly allowed claims of the Japanese patent application 2006-341650.

25

AMENDMENT

IN THE CLAIMS

Please amend the claims as follows.

1. (Amended based on original claim 1)

An access method for an apparatus to gain access
5 to a specific area assigned to an area number of a memory
device, the method comprising the steps of:

the apparatus transmitting designation
information including the area number, an access start
offset in the specific area associated with the area number
10 and a size of data to access and designating an access
area of the memory device, to the memory device;

the apparatus transmitting verification
information generated based on the designation
information, together with a processing command for the
15 access area;

the memory device verifying the verification
information using the designation information; and

the memory device executing the processing command
when the verification succeeds.

20

2. (Amended based on original claim 2)

An access method for an apparatus to gain access
to a specific area assigned to an area number of a memory
device, the method comprising the steps of:

25 the apparatus sharing a verification key with the
memory device;

the apparatus transmitting designation

information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an access area of the memory device, to the memory device;

5 the apparatus transmitting verification data obtained by encrypting the designation information using the verification key, together with a processing command for the access area;

10 the memory device verifying the verification data using the designation information and the verification key; and

the memory device executing the processing command when the verification succeeds.

15 3. (Amended based on original claim 3)

 An access method for an apparatus to gain access to a specific area assigned to an area number of a memory device, the method comprising the steps of:

20 the apparatus sharing a verification key with the memory device;

the apparatus requesting transmission of random numbers to the memory device;

the memory device transmitting random numbers to the apparatus;

25 the apparatus transmitting designation information including the area number, an access start offset in the specific area associated with the area number

and a size of data to access and designating an access area of the memory device, to the memory device;

the apparatus transmitting verification data obtained by encrypting verification information
5 including the random numbers and the designation information, together with a processing command for the access area;

the memory device verifying the verification data
using the random numbers, the designation information
10 and the verification key; and

the memory device executing the processing command
when the verification succeeds.

4. (Amended based on original claim 9)

15 A memory device whose data of a specific area assigned to an area number is read and written by an apparatus, the memory device comprising:

a processing command receiver that receives designation information including the area number, an
20 access start offset in the specific area associated with the area number and a size of data to access and designating
an area to access and receives verification information generated based on the designation information and a processing command including a command for read or write;

25 a designation information verifier that performs verification processing on the verification information
using the designation information;

a storage area that stores data;

a storage area access section that performs read or write from/in a designated area of the storage area according to the command for processing, when the verification processing succeeds;

a data transmitter that transmits data read by the storage area access section to the apparatus; and

a data receiver that receives data to write from the apparatus.

10

5. (New)

The memory device according to claim 4, wherein the designation information verifier generates random numbers in response to a request by the apparatus, holds the random numbers and transmits the random numbers to the apparatus.

15

6. (Amended based on original claim 10)

The memory device according to claim 5, wherein the designation information verifier performs the verification processing using the verification information and a verification key.

20

7. (Amended based on original claim 11)

The memory device according to claim 6, further comprising:

25

a verification key sharing section that shares the

verification key with the apparatus.

8. (Amended based on claim 13)

An information apparatus that reads and writes data
5 of a specific area assigned to an area number from/in
a memory device, the information apparatus comprising:

a designation information determiner that
determines designation information including an access
start offset for reading and writing data from/in the
10 specific area, a size of data for performing read and
write and the area number and designating the access area;

a verification information generator that performs
processing for generating verification information based
on the designation information;

15 a processing command transmitter that transmits
the designation information to the memory device and
separately transmits the verification information and
a processing command for reading or writing data;

a data transmitter that transmits data to the memory
20 device when the processing command is write; and

a data receiver that receives data from the memory
device when the processing command is read.

9. (New)

25 The information apparatus according to claim 8,
wherein the detection information verifier requests
transmission of random numbers to the memory device and

receives the random numbers from the memory device.

10. (Amended based on original claim 14)

5 The information apparatus according to claim 9,
wherein the verification information generator performs
the processing for generating the verification
information using the designation information and a
verification key.

10 11. (Amended based on original claim 15)

The information apparatus according to claim 10,
further comprising:

a verification key sharing section that shares the
verification key with the memory device.

15

12. (Amended based on original claim 17)

An access method for an apparatus to gain access
to a specific area assigned to an area number of a memory
device, the method comprising:

20 the apparatus transmitting designation
information including the area number, an access start
offset in the specific area associated with the area number
and a size of data to access and designating an access
area of the memory device, to the memory device;

25 the apparatus transmitting verification data
obtained by encrypting the designation information using
the verification key, together with a processing command

for the access area to the memory device;

the memory device verifying the verification data
using the designation information and the verification
key; and

5 the memory device executing the processing command
when the verification succeeds.

13. (Amended based on original claim 18)

 An access method for an apparatus to gain access
10 to a specific area assigned to an area number of a memory
 device, the method comprising the steps of:

the apparatus requesting transmission of random
 numbers to the memory device;

the memory device transmitting random numbers to
15 the apparatus;

the apparatus transmitting designation
 information including the area number, an access start
 offset in the specific area associated with the area number
 and a size of data to access and designating an access
20 area of the memory device, to the memory device;

the apparatus transmitting verification data
 obtained by encrypting the designation information using
 a verification key, together with a processing command
 for the access area to the memory device;

25 the memory device verifying the verification data
 using the random numbers, the designation information
 and the verification key; and

the memory device executing the processing command
when the verification succeeds.